

FIG. 1 - Prior Art

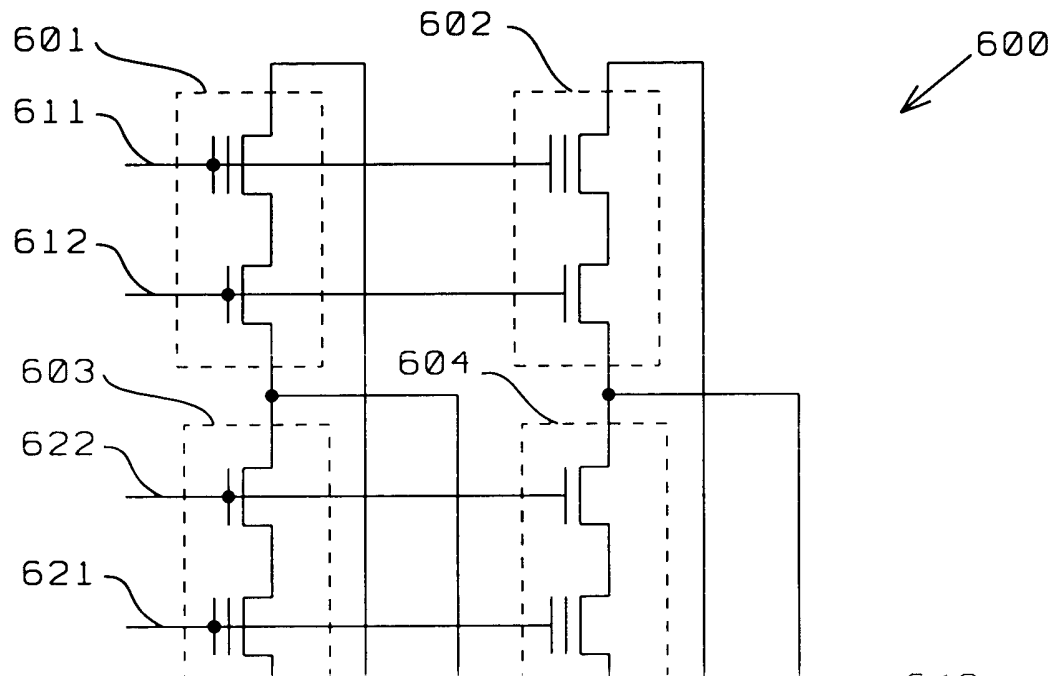


FIG. 2 - Prior Art

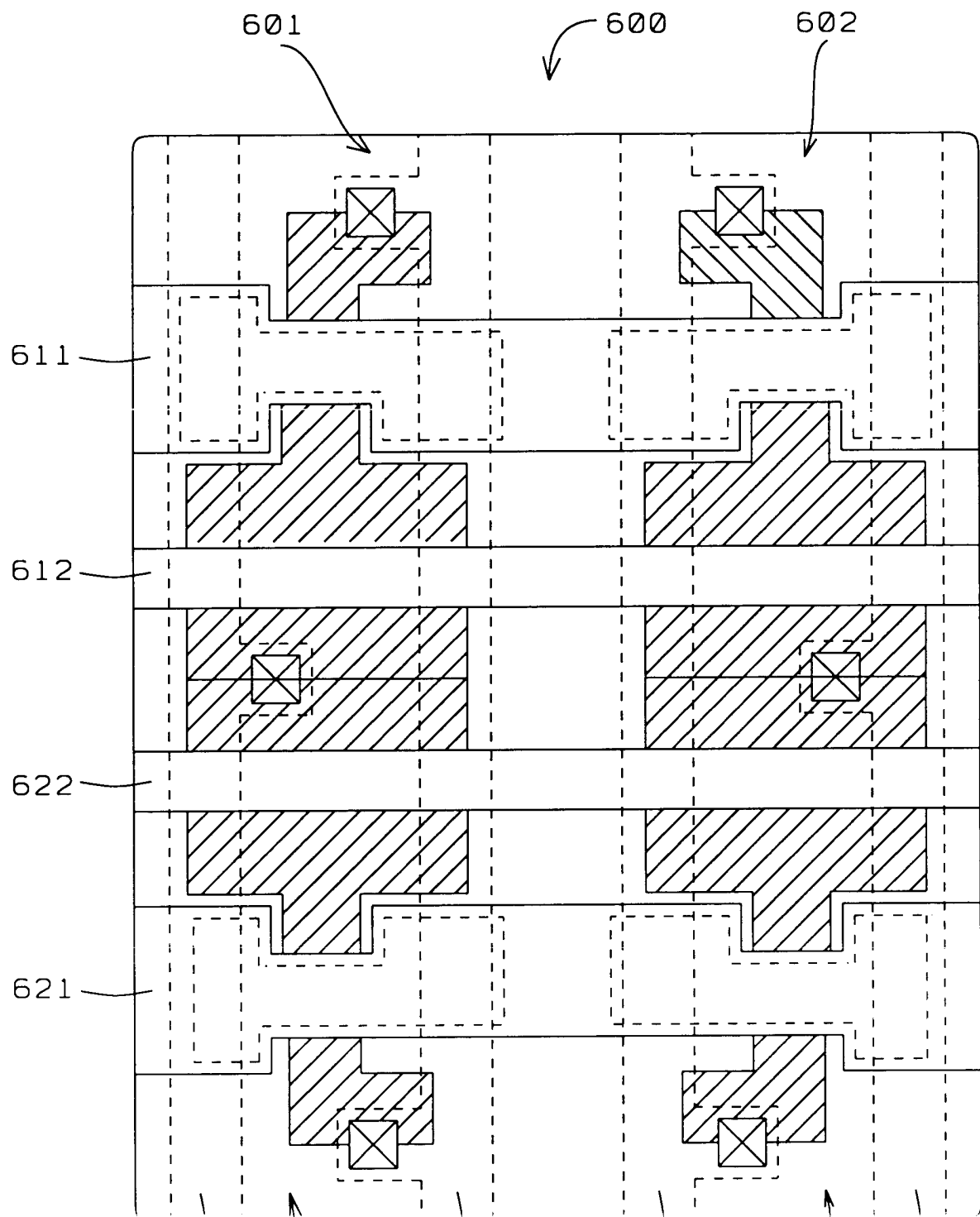


FIG. 3 - Prior Art

Selected WL Non-Selected WL

MO	Vcg	Vag	Vcg	Vag	V <sub>S</sub>	V <sub>d</sub>	V <sub>pwell</sub>	V <sub>nwell</sub>
PROC	-7 TO -11 VOLTS	8 VOLTS	0 VOLTS	0 VOLTS	HIGH Z	5 TO 8 VOLTS	0 VOLTS	3.3 VOLTS
PROC INHI	-7 TO -11 VOLTS	8 VOLTS	0 VOLTS	0 VOLTS	HIGH Z	0 VOLTS	0 VOLTS	3.3 VOLTS
ERR	8 TO 10 VOLTS	0 VOLTS			-8 TO -10 VOLTS	HIGH Z	8 TO 10 VOLTS	3.3 VOLTS
REF	3.3 VOLTS	3.3 VOLTS	0 VOLTS	0 VOLTS	0 VOLTS	1 VOLT	0 VOLTS	3.3 VOLTS

FIG. 4 - Prior Art

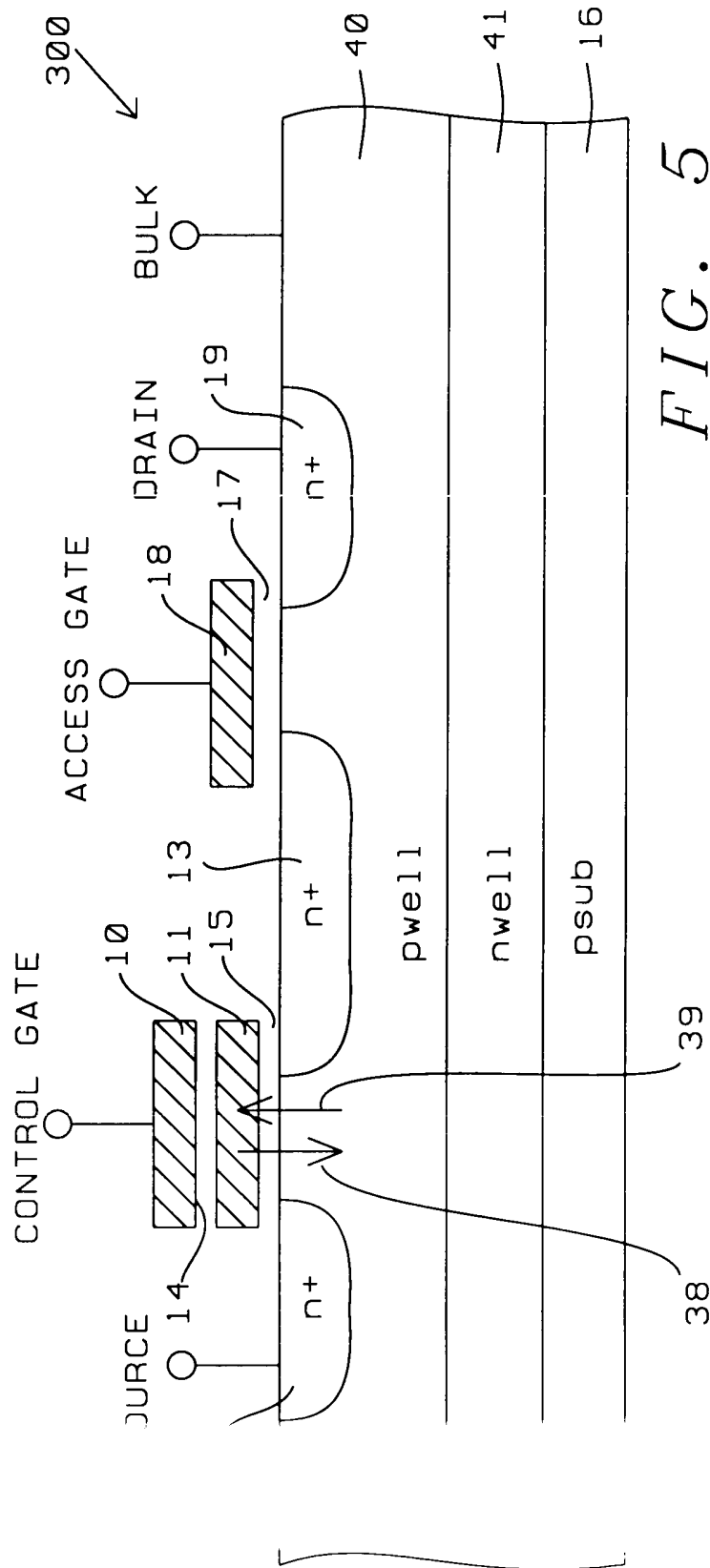




FIG. 6

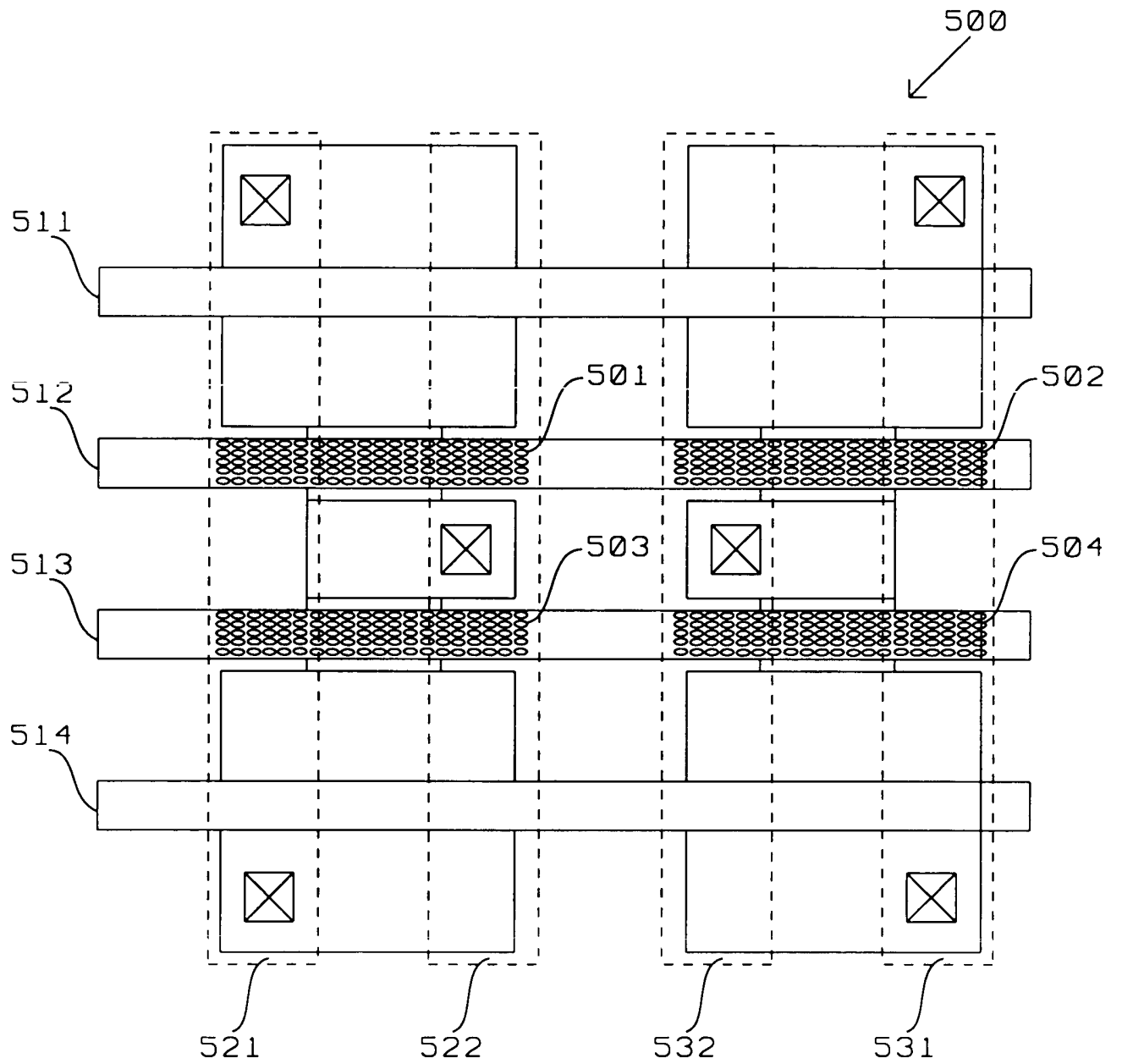


FIG. 7

	Selected WL		Non-Selected WL			
	Vcg	Vag	Vcg	Vag	Vs	Vd
ODE						V <sub>pswell</sub>
BASE	-10 VOLTS	0 VOLT	-	-	5 VOLTS	HIGH Z
GRAM	Vpgm	Vcc	-2.5 VOLTS	Vcc	HIGH Z	-5 VOLTS
GRAM IBIT	Vpgm	Vcc	-2.5 VOLTS	Vcc	HIGH Z	-5 VOLTS
AD	Vcc	Vcc	0 VOLTS	0 VOLTS	0 VOLTS	1 VOLT

FIG. 8a

	Selected WL		Non-Selected WL			
	Vcg	Vag	Vcg	Vag	Vs	Vd
ODE						V <sub>psub</sub>
BASE	-15 VOLTS	0 VOLT	-	-	0 VOLTS	HIGH Z
GRAM	Vpgm	8 VOLTS	2.5 VOLTS	8 VOLTS	HIGH Z	0 VOLTS
GRAM IBIT	Vpgm	8 VOLTS	2.5 VOLTS	8 VOLTS	HIGH Z	5 VOLTS
AD	Vcc	Vcc	0 VOLTS	0 VOLTS	0 VOLTS	1 VOLT

FIG. 8b